

Memory map of MiMicRemoteMCU/1.3.0

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This document expresses the memory map of MiMicRemoteMCU/1.3 on LPC1769.

General Use	Address range	MiMic use
system reserved	0x20084000-0xFFFFFFFF	
AHBSRAM(16k x2)	0x20080000-0x20083FFF	UserArea(16KB)
	0x2007C000-0x2007FFFF	TCP/IP stack memory(16KB)
system reserved	0x10080000-2007BFFF	
32KB local memory	0x10000000-0x1007FFFF	Local memory
system reserved	:	
	0x00080000-0x0FFFFFFF	
	0x00078000-0x0007FFFF	MiMicConfigurationArea(Sector29)
512k on-chip flash	0x00000000-0x00077FFF	MiMicApplication (Sector0-28)

1. The MiMicRemoteMCU uses Sector29 of on-chip flash memory as configuration storage. Therefor, User application size is restricted under 480KB.
2. MiMicRemoteMCU core program uses 96KB on-bord flash memory. Free area is about 384KB.
3. First 16KB of AHBSRAM(32K) is used by MiMicRemoteMCU TCP/IP stack. Second 16KB is free area for the user application.
4. Local memory is used by the application stack memory and FreeRTOS.

See UM10360.pdf for more information.

http://www.nxp.com/documents/user_manual/UM10360.pdf